

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Inventor(s): Inagaki et al.
 Atty.Dkt: 41226/278095
 Appln. No.: 09/830,360
 Filed: April 25, 2001
 Printed circuit board and method of manufacturing printed circuit board
 Date: July 8, 2003

Group Art Unit: 2827
 Examiner: Tuan T. Dinh



IDS LETTER CITING APPLICATION(S)

The Examiner's attention is directed to the following application(s) of:

Examiner's Initials*	FIRST INVENTOR (Last Name Only)	APPLICATION NO. & Filing Date	ENCLOSED
	Inagaki et al.	09/830,361 Apr 25, 2001	<input checked="" type="checkbox"/> Spec <input checked="" type="checkbox"/> Drawings <input checked="" type="checkbox"/> Current Claims <input type="checkbox"/> Other
			<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
			<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
			<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other

PLEASE DO NOT PRINT the above information on the patent which results from the subject application.

Consideration of each listed application is earnestly solicited since unpublished patent applications are contemplated as IDS material; see the exception in Rule 98(a)(2)(iii) and note the penultimate sentence of MPEP 609.

Further, in keeping with MPEP 609, Subsec. C(2), 2nd para., line 10 to end of the paragraph (especially note lines 18-25) **PLEASE RETURN A COPY OF THIS LETTER** with the Examiner's initials adjacent each above listing so that applicant will know that each listed application has been considered as required by PTO policy. Thanks.

Respectfully submitted

Glenn J. Perry, Reg. No. 28,458
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GJP/mll
 Pillsbury Winthrop LLP

*The Examiner's initials adjacent a citation indicates he/she has considered the cited application relative to the subject application.

09/830361

APPLICATION UNDER UNITED STATES PATENT LAWS

Atty. Dkt. No. PW 279059/7049P-U
(M#)

Invention: PRINTED CIRCUIT BOARD AND METHOD FOR MANUFACTURING PRINTED CIRCUIT
BOARD

Inventor (s): INAGAKI, Yasushi
ASAI, Motoo
WANG, Dongdong
YABASHI, Hideo
SHIRAI, Seiji

Do Not Enter.
For IDS
Purposes only -

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This is a:

- Provisional Application
- Regular Utility Application
- Continuing Application
 - The contents of the parent are incorporated by reference
- PCT National Phase Application
- Design Application
- Reissue Application
- Plant Application
- Substitute Specification
 - Sub. Spec Filed _____ / _____
in App. No. _____ / _____
- Marked up Specification re
Sub. Spec. filed _____ / _____
In App. No. _____ / _____

SPECIFICATION